



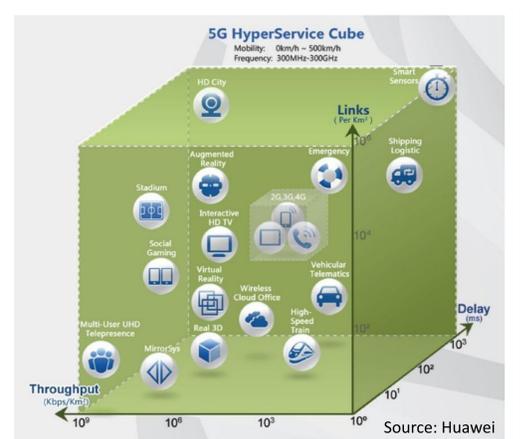
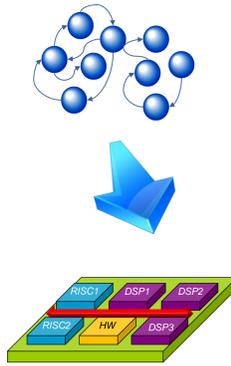
FP7 Coordination and Support Action to fund 50 technology transfer projects (TTP) in computing systems. This project has received funding from the European Union's Seventh Framework Programme for research, technological development and demonstration under grant agreement n° 609491.

## Multicore Platform SW Optimization with the MAPS Compiler

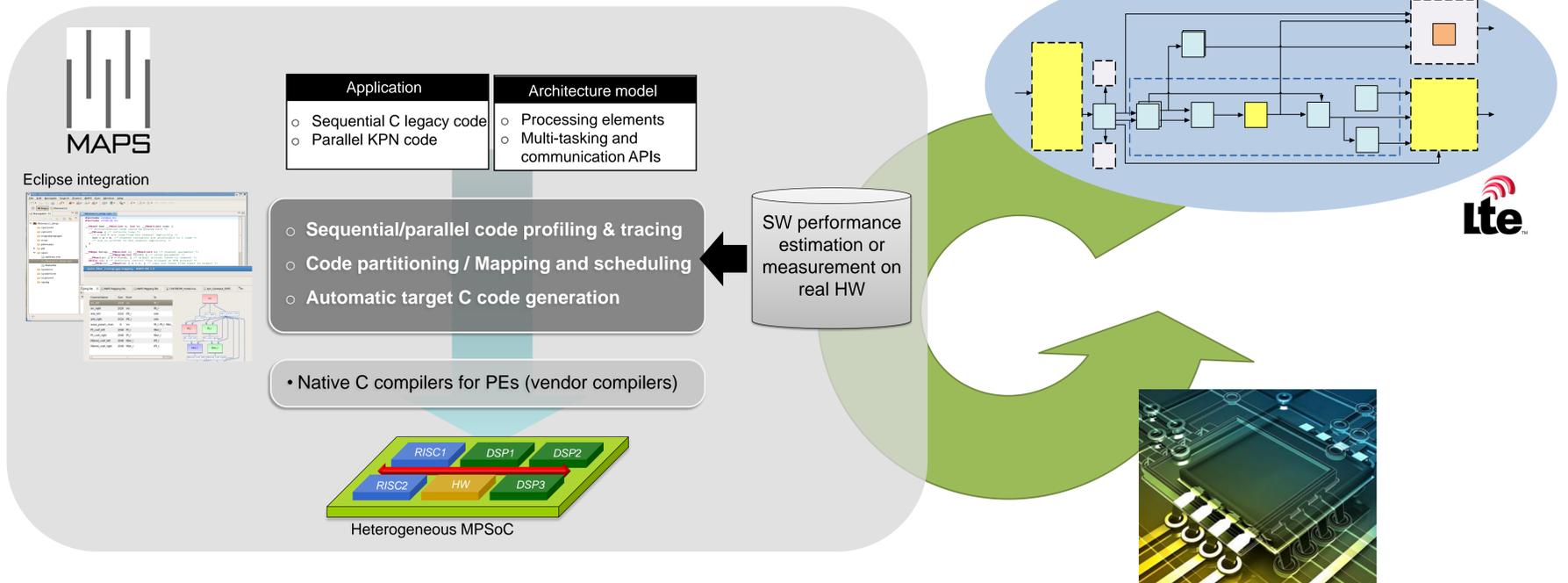
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### TTP Problem

- ❑ The complexity of **designing next generation of wireless base stations** is growing exponentially
  - Significantly more computing power required
  - Timing / Power / Energy as co-optimization goals
  - Application dynamism (e.g. multi-mode, scenarios)
- ❑ TTP focuses on improving software development **productivity, quality of results, and portability** for Huawei SoC platforms.



### TTP Solution



### TTP Impact

- ❑ Improve Huawei's capabilities of performing **fast and efficient software mapping** for its future multicore System-on-Chip Platforms, especially in the wireless domain.
- ❑ Improve the **productivity** of multicore software design by providing a higher degree of automation.
- ❑ **Real-world** testing for academic works "MAPS" in industrial environment.
- ❑ Facilitate **joint** publications / demos in international conferences and events.

### TTP Facts

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