

## **TETRACOM: Technology Transfer in Computing Systems**



FP7 Coordination and Support Action to fund 50 technology transfer projects (TTP) in computing systems. This project has received funding from the European Union's Seventh Framework Programme for research, technological development and demonstration under grant agreement n<sup>o</sup> 609491.

## IP-DIME: Image Processing to Detect Hidden Defects in Manufactured Electronics Systems

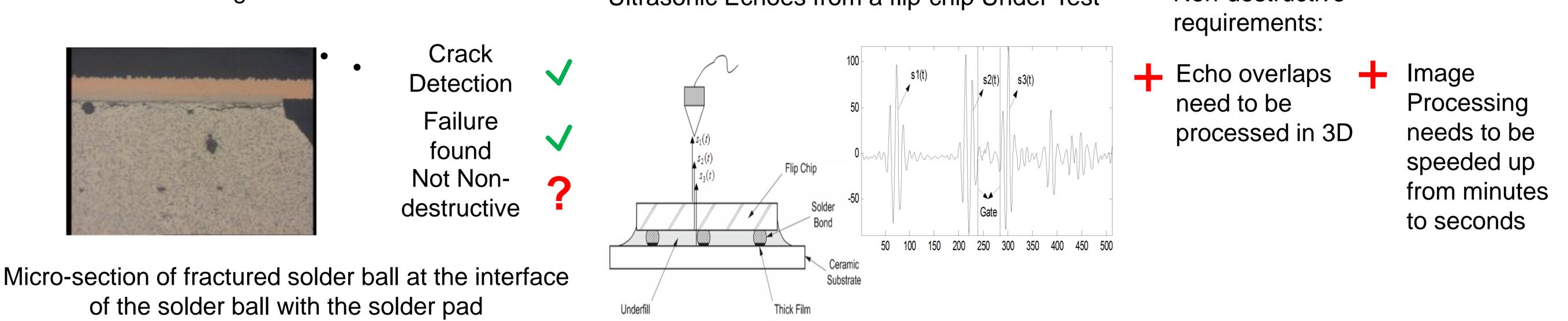
David M. Harvey, Guang-Ming Zhang, GERI, Liverpool John Moores University, UK Derek R. Braden, Delphi, UK, Europe and USA

How to inspect hidden solder joints/bonds through life ?

**TTP Problem** 

Ultrasonic Echoes from a flip-chip Under Test

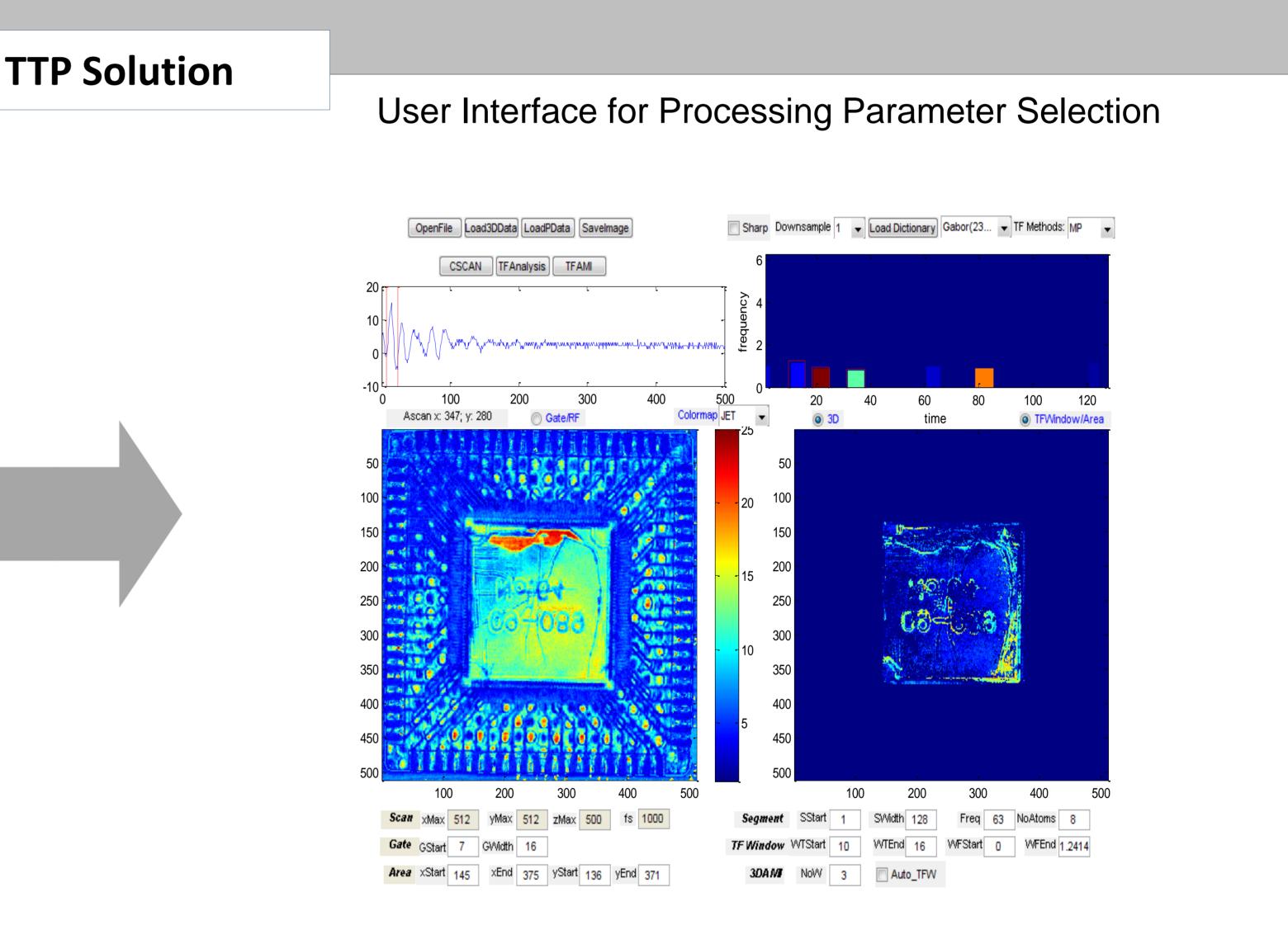
Non-destructive

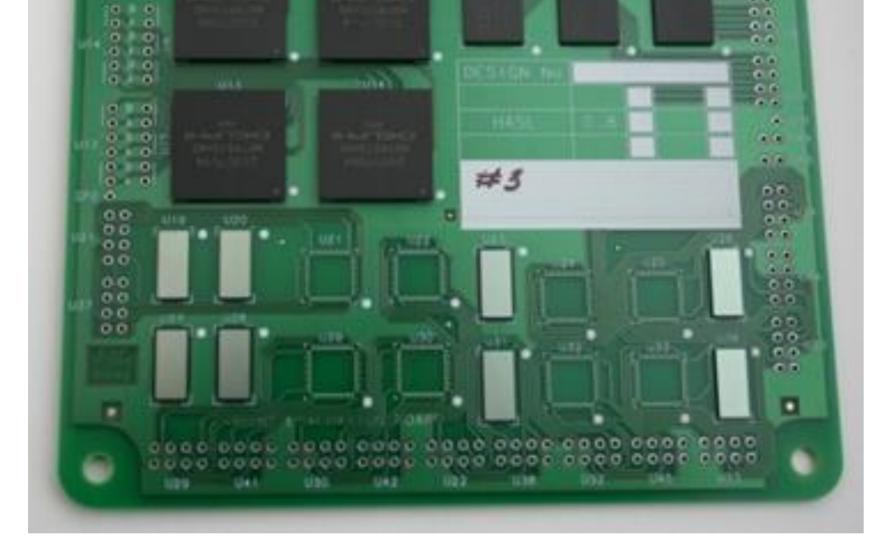


Sample of double sided test board designed to investigate flip-chip and BGA solder lifetimes

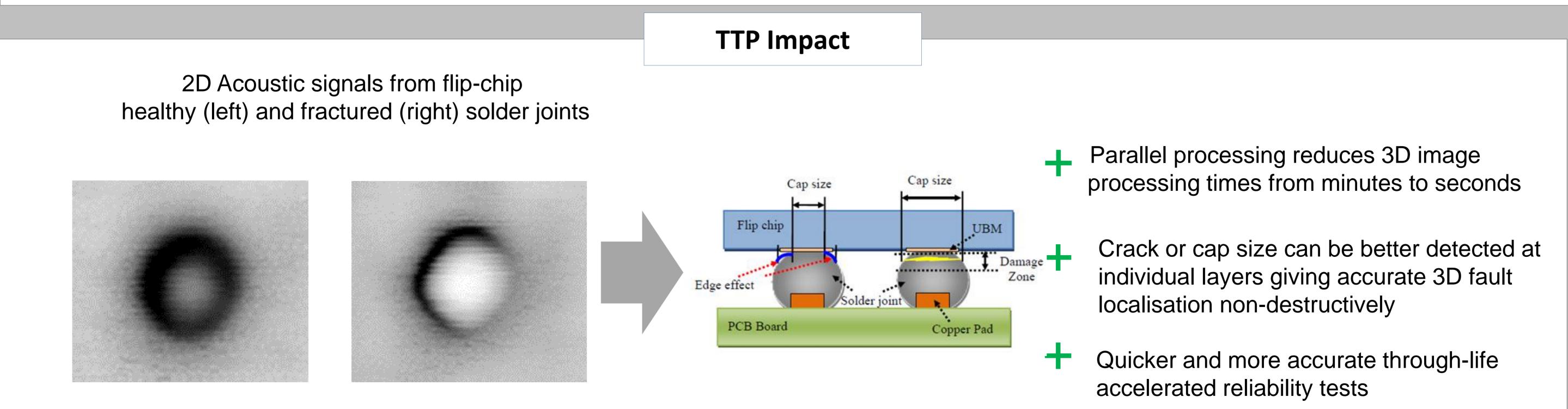
000000 000000

0000





User Windows for acoustic A-scan, C-Scan, Algorithm tuning and layer control



Contact: David Harvey E-mail: d.m.harvey@ljmu.ac.uk TETRACOM contribution: 37,096 EUR Duration: 01/09/2015-31/07/2016



